Senior FPGA/ASIC Verification Engineer

A little about us

Numascale develops unique hardware and software for the most challenging computing workloads.

Our technology has a legacy in decades of Norwegian CPU development and we are delivering word-class products to the highly competitive global computer market.

The challenge of making world class high speed electronics enabled Numascale to attract the finest engineers. This engineering team has delivered the enabling technology for the world’s largest computer running under one instance of the operating system, with more than 5000 cores and 21 Terabytes of RAM.

Due to high demand for new solutions Numascale is expanding, and is looking for 10 engineers who wants to work at the forefront of the computing industry.

Our Hardware Team

We develop next generation low latency, high bandwidth switches for cache-coherent shared memory servers and have to push the boundaries for performance in both FPGAs and state of the art ASIC technologies.

Dependent on your skillset and personal preferences, you will be working with system architecture, coherency protocols, processor design, memory controllers, serial transceivers, network protocols, etc.

We mainly use Synopsys VCS, Synopsys DC Compiler, Synplify, Altera Quartus and Xilinx Vivado.

The preferred working location is with the main team at our headquarters in Oslo. The right candidate will be given the option to telecommute.

A lot about you

In the position as Senior FPGA/ASIC Verification Engineer you will take part in a close-knit, agile team environment and be an active part of all aspects and phases of the development and verification.

Your assignments will include specification and implementation of tests, evaluate and improve testability of RTL, develop BFM s, interfacing 3rd party verification IP, taking part in reviews and maintaining systems for continuous integration and regression testing. You will need to focus on reuse by ensuring you cooperate well with the team and deliver efficient, well documented and robust code.

As Senior FPGA/ASIC Verification Engineer you are expected to take initiative and an active part of the verification efforts at Numascale.
To qualify you need to have the following skills and experience:

- Bachelor degree in Electrical Engineering, Computer Science or equivalent
- At least 5 years of experience doing high end FPGA/ASIC verification and/or design
- Attention to detail
- Self-motivated
- Strong work ethics
- Good cooperation and sharing skills
- Effortless communication in English, both written and spoken

You'll get additional credit for:

- Master degree, or equivalent, with relevant scope
- Continuous Integration Testing, using Jenkins or equivalent
- Knowledge of HyperTransport, QPI or equivalent protocols
- SystemVerilog, SystemC or UVM
- Assertion Based Verification
- Functional Coverage Verification
- Perl/TCL/Python/shell scripting
- Digital design experience
- Low level software programming
- Deep and sincere interest in our kind of technology

Please send your CV and application to: jobs@numascale.com