NUMASCALE

Job Category: Engineering
Job Location: Ahmedabad, Gujarat, INDIA
Job Type: Experienced or New College Graduate Hire

1. ASIC Verification Engineer (Experience 2-5 years)

Job Description

As a part of the verification team, you will use your design and verification expertise to verify complex digital designs. You will collaborate closely with design and verification engineers in active projects and perform hands-on verification. Using your UVM and System Verilog coding and problem-solving skills, you will build efficient and effective constrained-random verification environments that exercise designs through their corner-cases and expose all types of bugs. You will be responsible for the full life cycle of verification, from verification planning to test execution, to collecting and closing coverage.

Required Qualifications:
- BTech/BE/MTech/ME/MS - Electronics/Telecommunication/computer science or equivalent
- 2 to 5 years of experience in IP level or SOC level verification using System Verilog and OVM/UVM
- A solid understanding of object-oriented concepts and experience designing class-based test benches
- Should have participated in successful completion of at least one ASIC/SoC project from Specifications to Silicon.
- Expert in coding SV Testbench, drivers, monitors, scoreboards, checkers
- Expert in using verification tools like VCS, modelsim, etc
- Expert in System Verilog Assertions, Code and Functional Coverage and Formal verification techniques.
- Should have experience in regression debugs, Coverage analysis
- Expertise in scripting languages such as PERL, TCL, Python.
- Ability to communicate with architecture, RTL design and other remote teams
- Excellent verbal and written communication skills
- Protocol Knowledge on PCIe, Cache Coherency, Ethernet and DDR3/DDR4 is an added advantage.

2. ASIC Verification Engineer (Fresher)

Job Description

As a part of the verification team, you will get a chance to demonstrate your programming skills to develop block level verification environment to verify internal IPs and sub-blocks of the design. You will be working closely with senior verification engineers to get trained on existing verification environment. You will be responsible to create verification IPs, test plans, execute test scenarios, functional coverage analysis and writing system Verilog Assertions.

Required Qualifications:

- BTech/BE/MTech/ME/MS - Electronics/Telecommunication/computer science or equivalent
- A solid understanding of object-oriented concepts and experience designing class-based test benches
- Hands on experience in Block level verification using System Verilog
- Very good debugging / problem solving skills.
- Familiar with verification tools like VCS, modelsim etc
- Expertise in scripting languages such as PERL, TCL, Python
- Ability to communicate with architecture, RTL design and other remote teams
- Excellent verbal and written communication skills
- Experience with OVM/UVM is an added advantage.
- Experience in System Verilog Assertions, Code and Functional Coverage and Formal verification techniques is a plus.

About Numascale:

Numascale (www.numascale.com) is the central enabler of large scale data intensive computing solution in the industry. Developing ASIC component using Intel cache coherent technology that enables to build single node systems with high CPU socket count with large coherent memory foot print that produces highly efficient computing throughput. Numascale delivered the enabling technology for the world's largest computer running under one instance of the operating system, with more than 5000 cores and 21 Terabytes of RAM.

Scale-Up computing demand is growing in enterprise and in cloud segments. Numascale engineering team needs to grow to keep up with this computing paradigm shift. Numascale is looking for enthusiastic and passionate engineers who want to embrace the challenge and be part of the upcoming exciting world of Scale-Up computing. Join the team of highly energetic engineers who does not take "no" for an answer and always find the "yes" for any engineering solution to achieve the goals. You will be part of an open and flexible team with high degree of professional work ethic.

Please send your CV and application to: vb@numascale.com